WEST

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| Main Menu | Search Form | Posting Counts | Show S Numbers | Edit S Numbers | Preferences | Cases |

Search Results -

Terms	Documents
L11 and (execut$ near4 (order$ or sequence$))	15

Database:

```
US Patents Full-Text Database
US Pre-Grant Publication Full-Text Database
JPO Abstracts Database
EPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
```

Search:

```
L12
```

| Refine Search |

| Recall Text | Clear |

Search History

DATE: Wednesday, May 14, 2003   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
	DB=USPT; PLUR=YES; OP=ADJ		
L12	L11 and (execut$ near4 (order$ or sequence$))	15	L12
L11	15 and ((input$ near4 register$) and (output$ near4 register$))	28	L11
L10	L9 and (order$ or sequenc$) near5 execut$	0	L10
L9	L8 and execut$	1	L9
L8	5175863.pn.	1	L8
L7	L6 and ((Input$ or source$)near4 operand$)	3	L7
L6	L5 and register$	55	L6
L5	((reus$ near4 compute) or (reus$ near4 computation$))	109	L5
L4	L3 and (live in and live out) and register$	0	L4
L3	(reus$ near4 comput$)	450	L3
L2	L1 and map$ same (many to one or many-to-one)	2	L2
L1	(reus$ near4 comput$)	450	L1

WEST

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Search Results -

Terms	Documents
reus$ near4 computation$	89

Database:

- **US Patents Full-Text Database**
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- JPO Abstracts Database
- EPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Search:

```
L13
```

| Refine Search |

| Recall Text | Clear |

Search History

DATE: Wednesday, May 14, 2003   Printable Copy   Create Case

Set Name	Query	Hit Count	Set Name
	side by side		result set

DB=USPT; PLUR=YES; OP=ADJ

Set Name	Query	Hit Count	Set Name
L13	reus$ near4 computation$	89	L13
L12	L11 and (execut$ near4 (order$ or sequence$))	15	L12
L11	l5 and ((input$ near4 register$) and (output$ near4 register$))	28	L11
L10	L9 and (order$ or sequenc$) near5 execut$	0	L10
L9·	L8 and execut$	1	L9
L8	5175863.pn.	1	L8
L7	L6 and ((Input$ or source$)near4 operand$)	3	L7
L6	L5 and register$	55	L6
L5	((reus$ near4 compute) or (reus$ near4 computation$))	109	L5
L4	L3 and (live in and live out) and register$	0	L4
L3	(reus$ near4 comput$)	450	L3
L2	L1 and map$ same (many to one or many-to-one)	2	L2
L1	(reus$ near4 comput$)	450	L1

END OF SEARCH HISTORY